

FIG. 1A

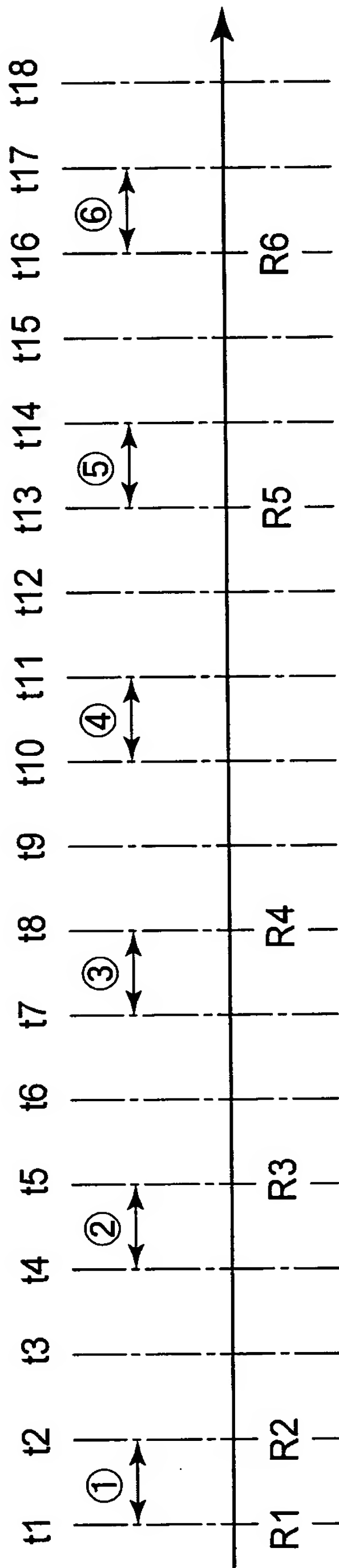


FIG. 1B

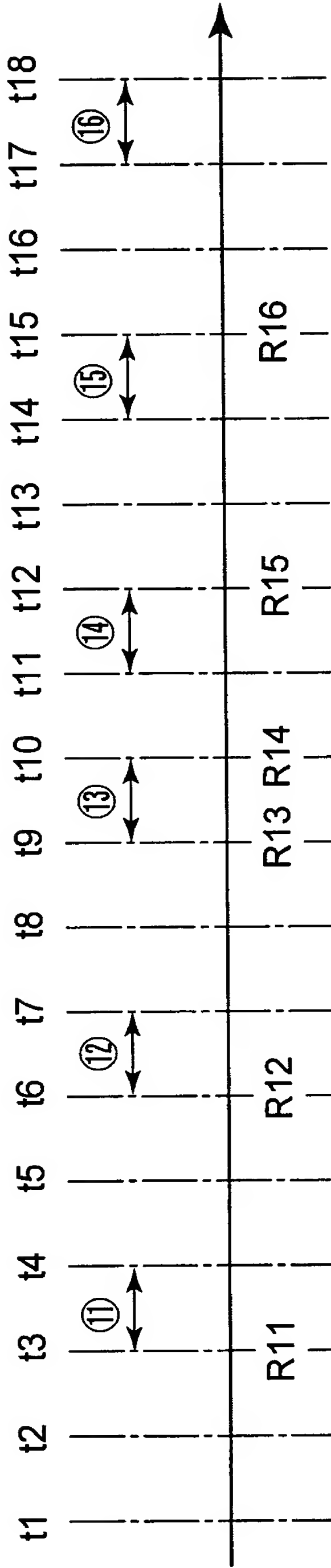


FIG. 2

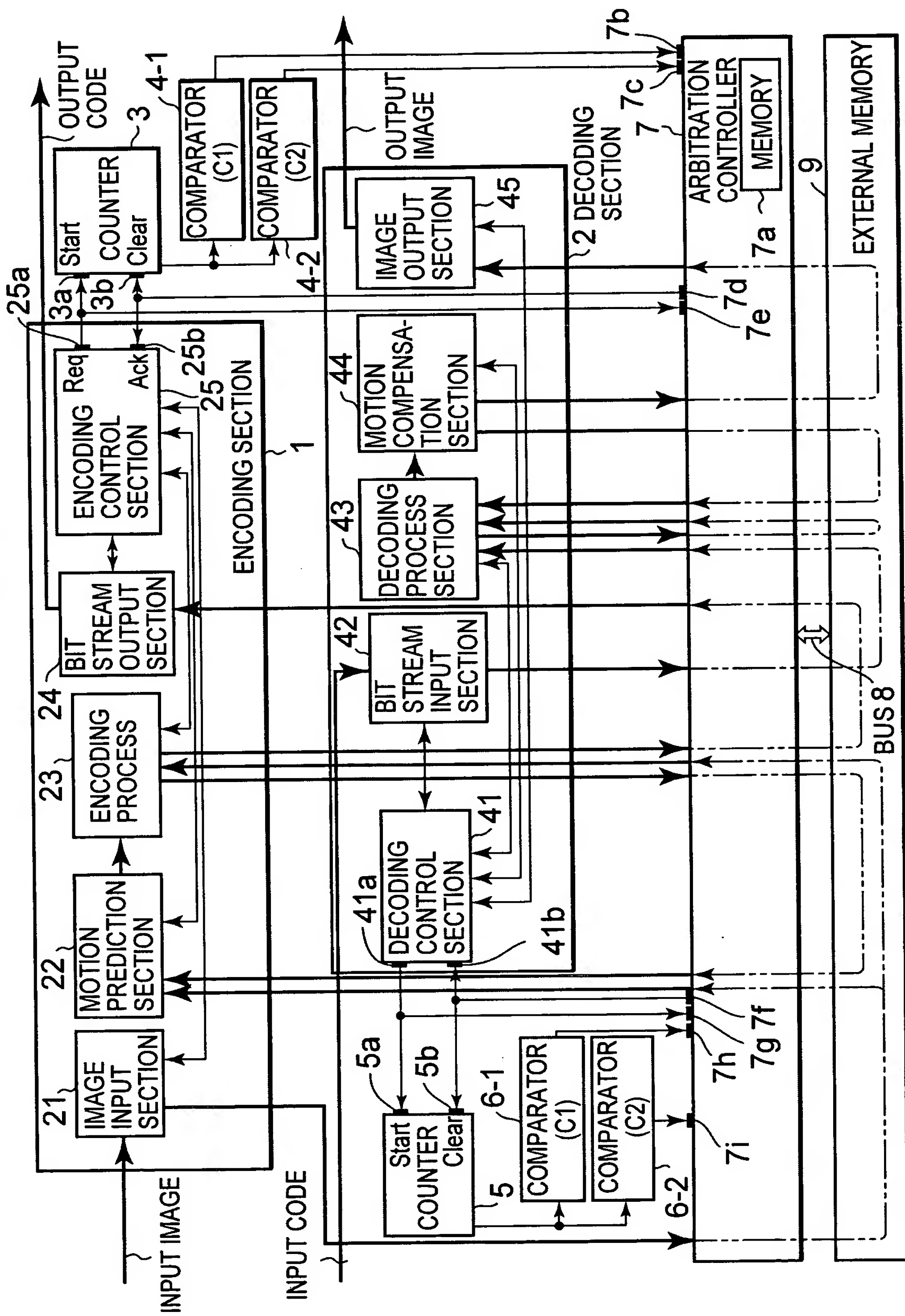


FIG. 3

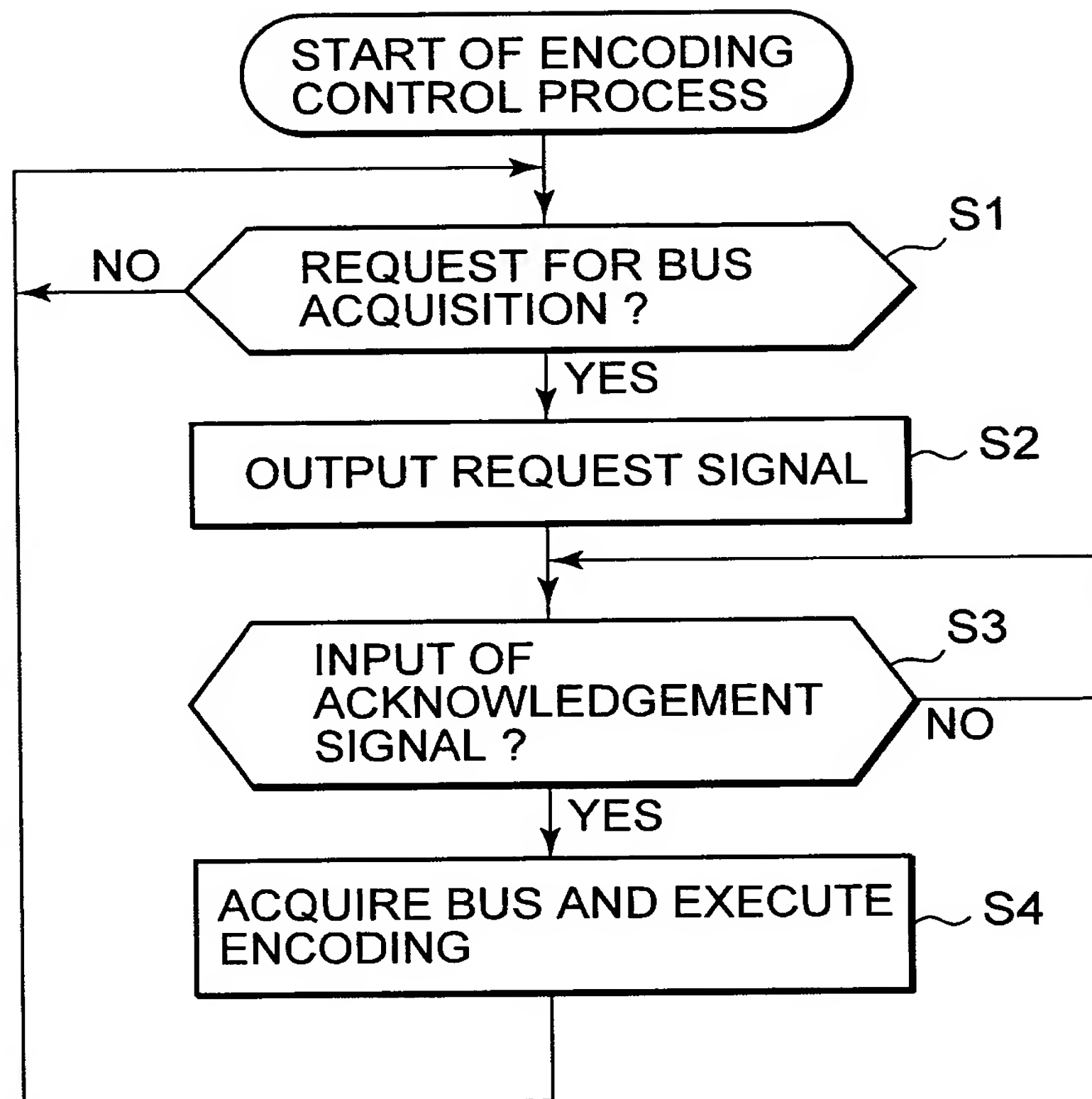


FIG. 4

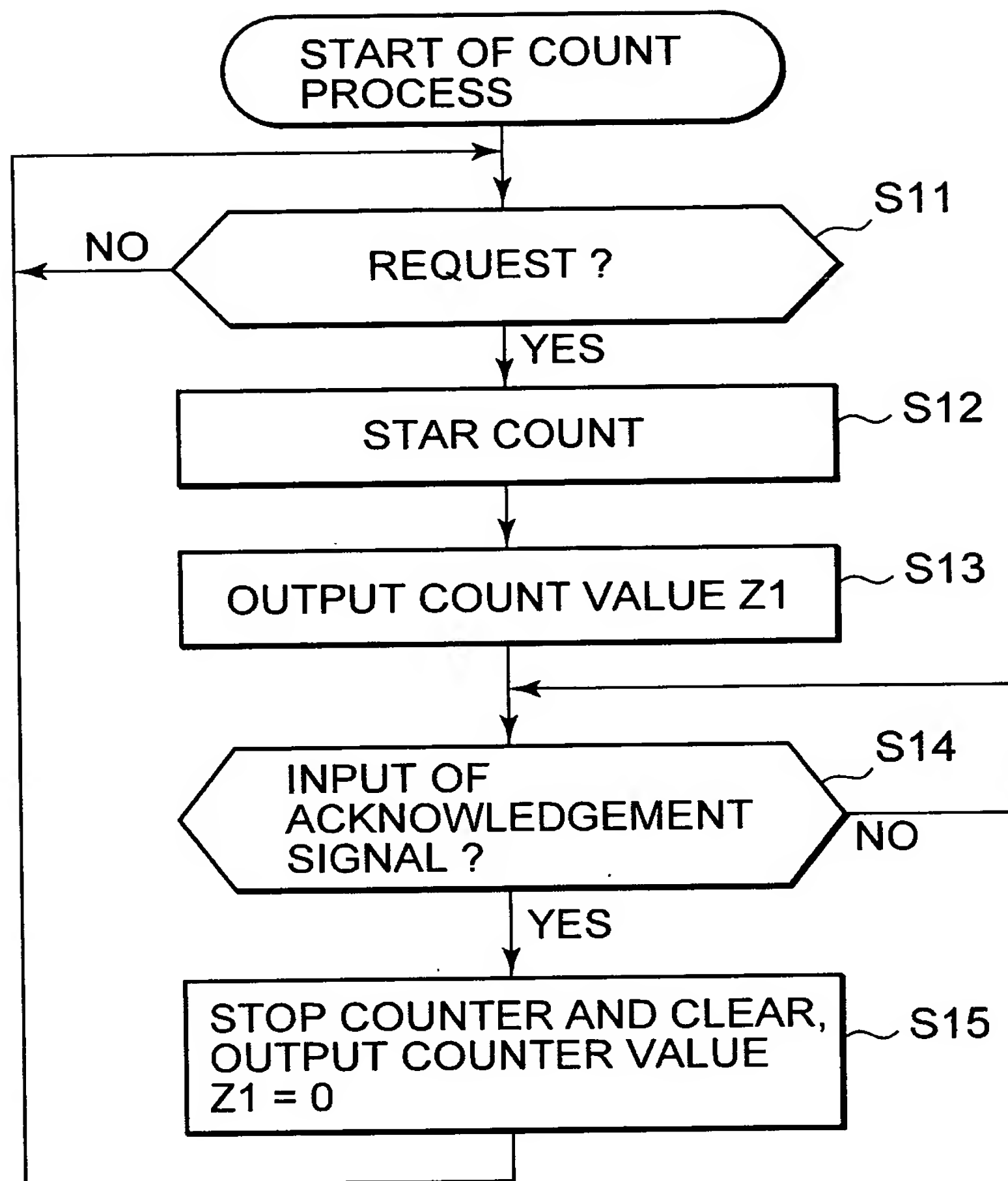


FIG. 5

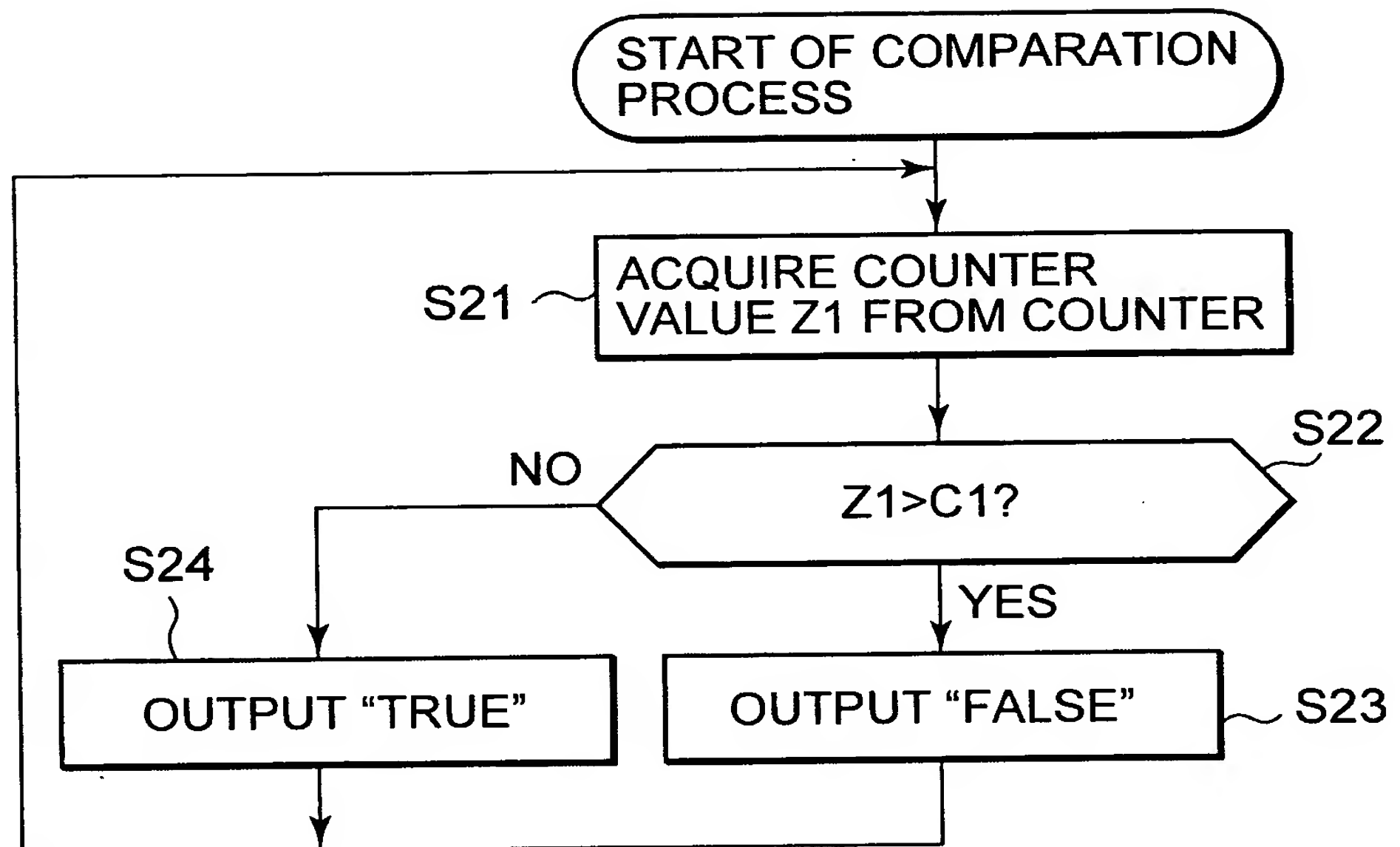


FIG. 6

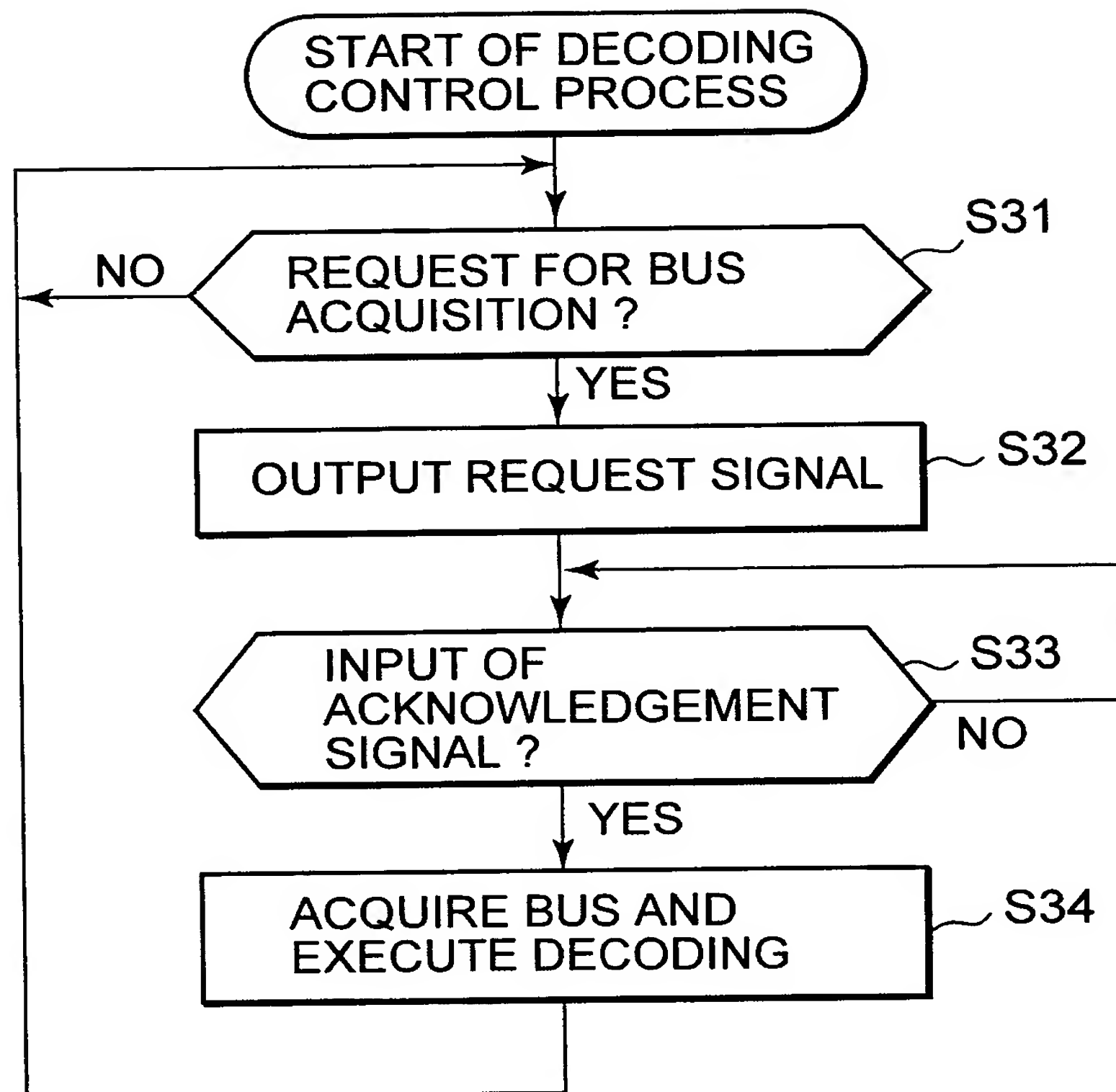


FIG. 7

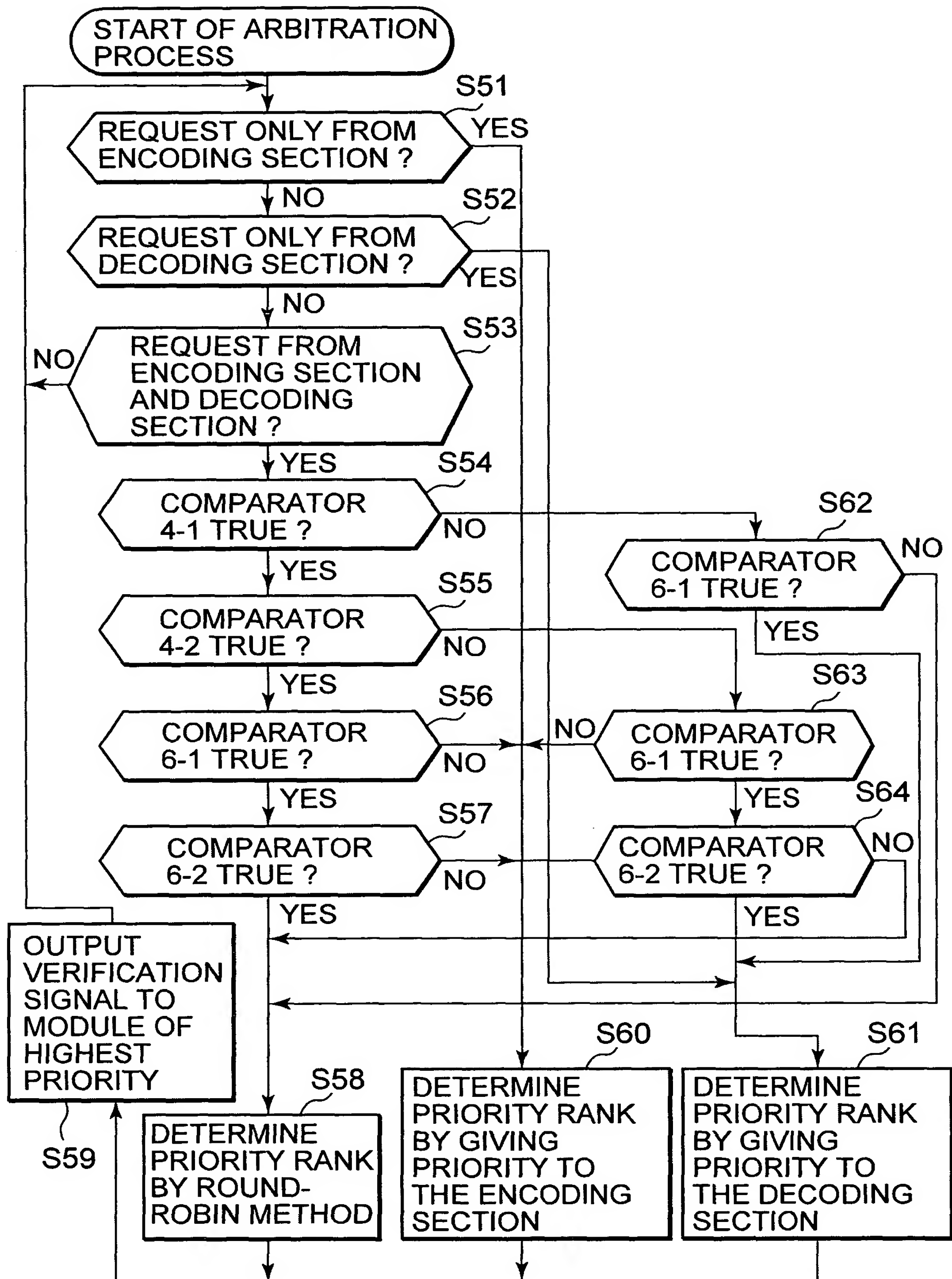


FIG. 8A

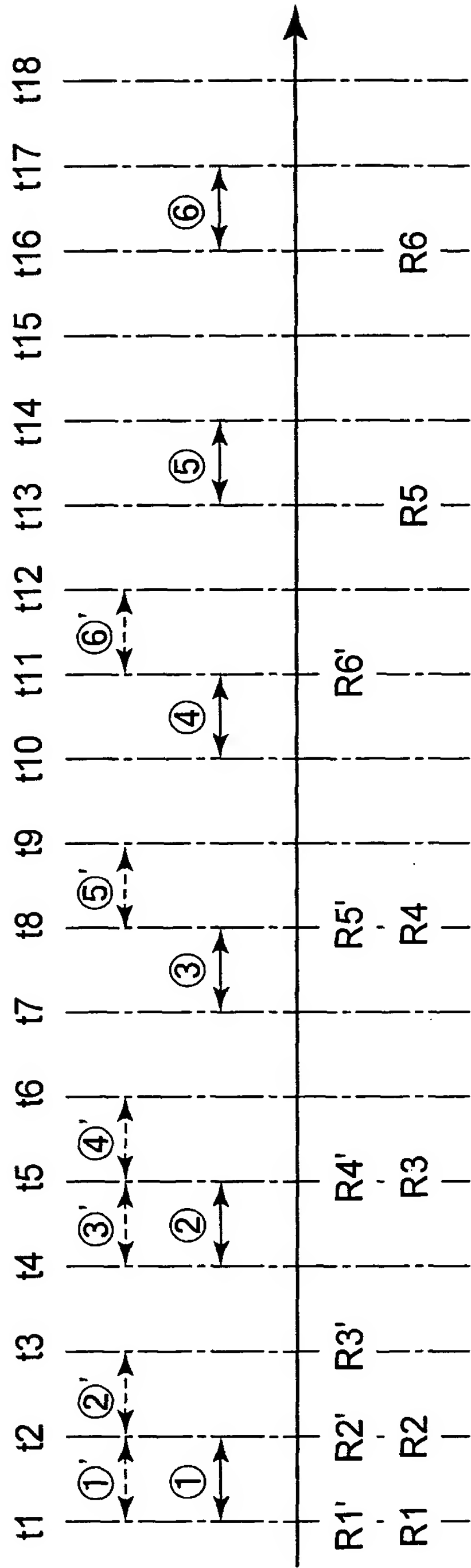


FIG. 8B

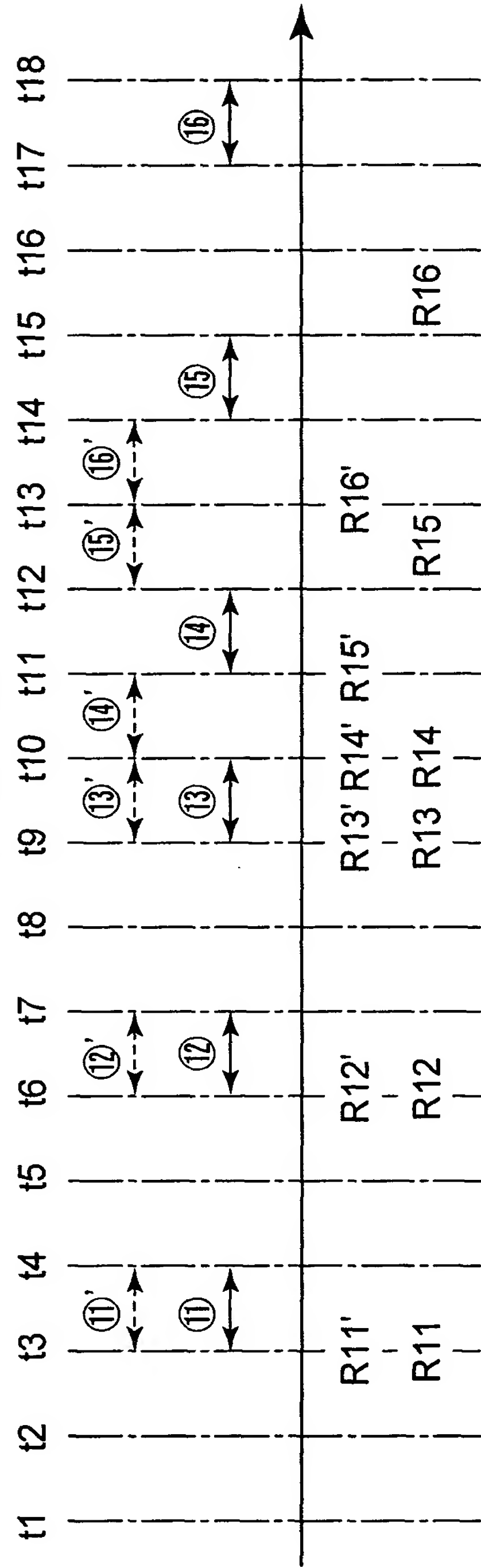


FIG. 9

	ENCODING SECTION (M0)		DECODING SECTION (M1)	
	Comp4-1	Comp4-2	Comp6-1	Comp6-2
R(M0,M1)	0	0	0	0
	1	0	1	0
	1	1	1	1
M0,M1	1	0	0	0
	1	1	0	0
	1	1	1	0
M1,M0	1	0	1	1
	0	0	1	1
	0	0	1	0

FIG. 10

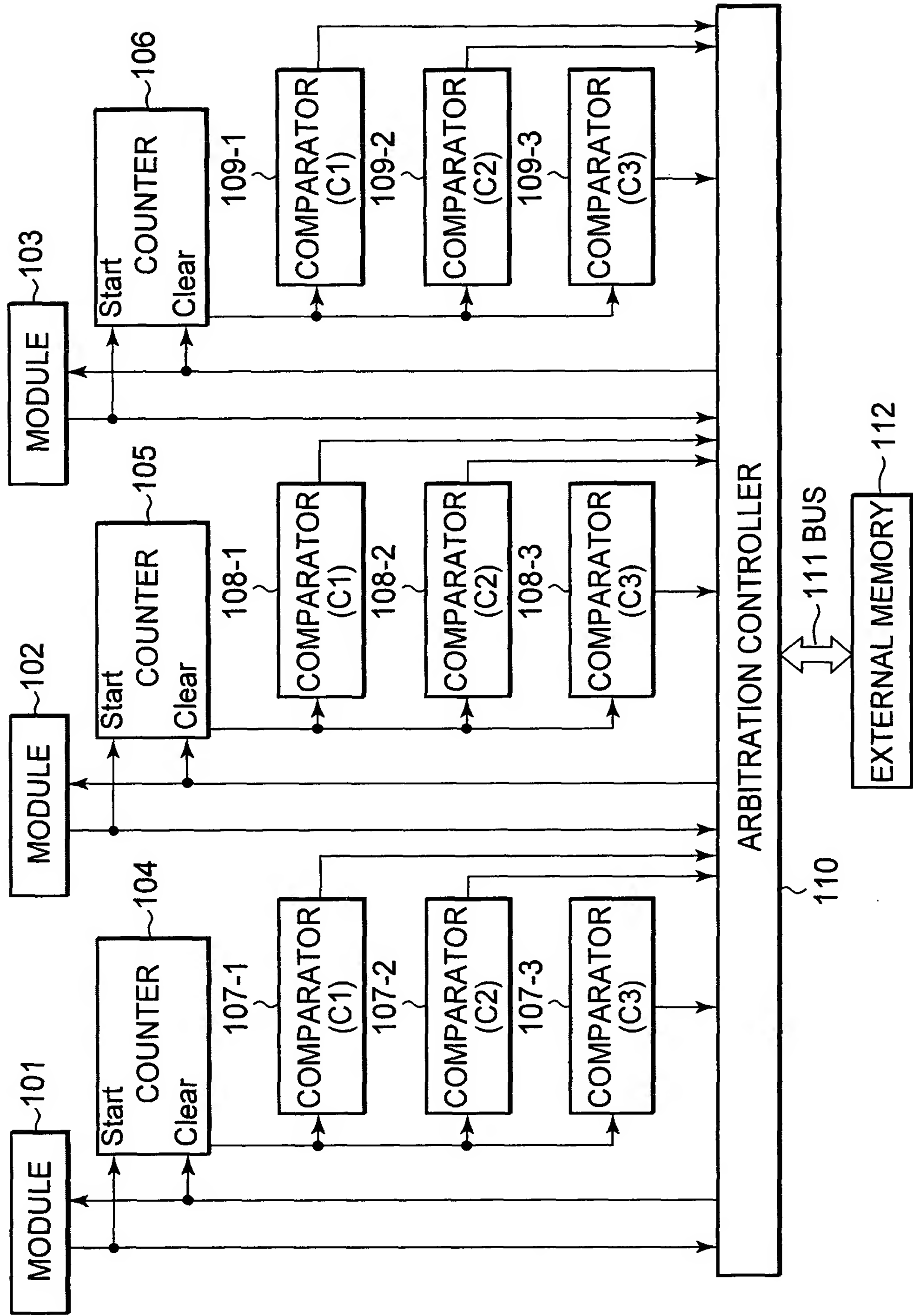


FIG. 11

	M0			M1			M2		
	Comp107-1	Comp107-2	Comp107-3	Comp108-1	Comp108-2	Comp108-3	Comp109-1	Comp109-2	Comp109-3
R(M0,M1,M2)	0	0	0	0	0	0	0	0	0
	1	0	0	1	0	0	1	0	0
	1	1	0	1	1	0	1	1	0
	1	1	1	1	1	1	1	1	1
M0,R(M1,M2)	1	0	0	0	0	0	0	0	0
	1	1	0	0	0	0	0	0	0
	1	1	0	1	0	0	1	0	0
	1	1	1	0	0	0	0	0	0
	1	1	1	1	0	0	1	0	0
	1	1	1	1	1	0	0	0	0
	1	1	1	1	1	0	0	0	0
R(M1,M2)M0	0	0	0	1	0	0	1	0	0
	0	0	0	1	1	0	1	1	0
	0	0	0	1	1	1	1	1	1
	1	0	0	1	1	0	1	1	0
	1	0	0	1	1	1	1	1	1
	1	0	0	1	1	1	1	1	1
	1	1	0	1	0	0	0	0	0
M0,M1,M2	1	1	0	1	0	0	0	0	0
	1	1	1	1	0	0	0	0	0
	1	1	1	1	1	0	0	0	0
	1	1	1	1	1	0	1	0	0
M0,M2,M1	1	1	0	0	0	0	1	0	0
	1	1	1	0	0	0	1	0	0
	1	1	1	0	0	0	1	0	0
	1	1	1	1	0	0	1	1	0

FIG. 12

	M0			M1			M2		
	Comp107-1	Comp107-2	Comp107-3	Comp108-1	Comp108-2	Comp108-3	Comp109-1	Comp109-2	Comp109-3
M1,R(M0,M2)	0	0	0	1	0	0	0	0	0
	0	0	0	1	1	0	0	0	0
	1	0	0	1	1	0	1	0	0
	0	0	0	1	1	1	0	0	0
	1	0	0	1	1	1	1	0	0
	1	1	0	1	1	1	1	1	0
R(M0,M2),M1	1	0	0	0	0	0	1	0	0
	1	1	0	0	0	0	1	1	0
	1	1	1	0	0	0	1	1	1
	1	1	0	1	0	0	1	1	0
	1	1	1	1	0	0	1	1	1
	1	1	1	1	0	0	1	1	1
M1,M0,M2	1	0	0	1	1	0	0	0	0
	1	0	0	1	1	1	0	0	0
	1	1	0	1	1	1	0	0	0
	1	1	0	1	1	1	1	0	0
M1,M2,M0	0	0	0	1	1	0	1	0	0
	0	0	0	1	1	1	1	0	0
	0	0	0	1	1	1	1	1	0
	1	0	0	1	1	1	1	1	0

FIG. 13

	M0			M1			M2		
	Comp107-1	Comp107-2	Comp107-3	Comp108-1	Comp108-2	Comp108-3	Comp109-1	Comp109-2	Comp109-3
M2,R(M0,M1)	0	0	0	1	0	0	1	0	0
	0	0	0	1	0	0	1	1	0
	1	0	0	1	0	0	1	1	0
	0	0	0	0	0	0	1	1	1
	1	0	0	1	0	0	1	1	1
	1	1	0	1	1	0	1	1	1
	1	0	0	1	0	0	0	0	0
R(M0,M1),M2	1	1	0	1	1	0	0	0	0
	1	1	1	1	1	1	0	0	0
	1	1	1	1	1	1	0	0	0
	1	1	0	1	1	0	1	0	0
	1	1	1	1	1	1	1	1	0
	1	0	0	0	0	0	1	1	0
	1	0	0	0	0	0	1	1	0
M2,M0,M1	1	0	0	0	0	0	1	1	0
	1	0	0	0	0	0	1	1	1
	1	1	0	0	0	0	1	1	1
	1	1	0	1	0	0	1	1	1
M2,M1,M0	0	0	0	1	0	0	1	1	0
	0	0	0	1	0	0	1	1	1
	1	0	0	1	1	0	1	1	1
	0	0	0	1	1	0	1	1	1

FIG. 14

